

APPLICATION NOTE

APN1007: Switchable Dual-Band 170/420 MHz VCO for Handset Cellular Applications

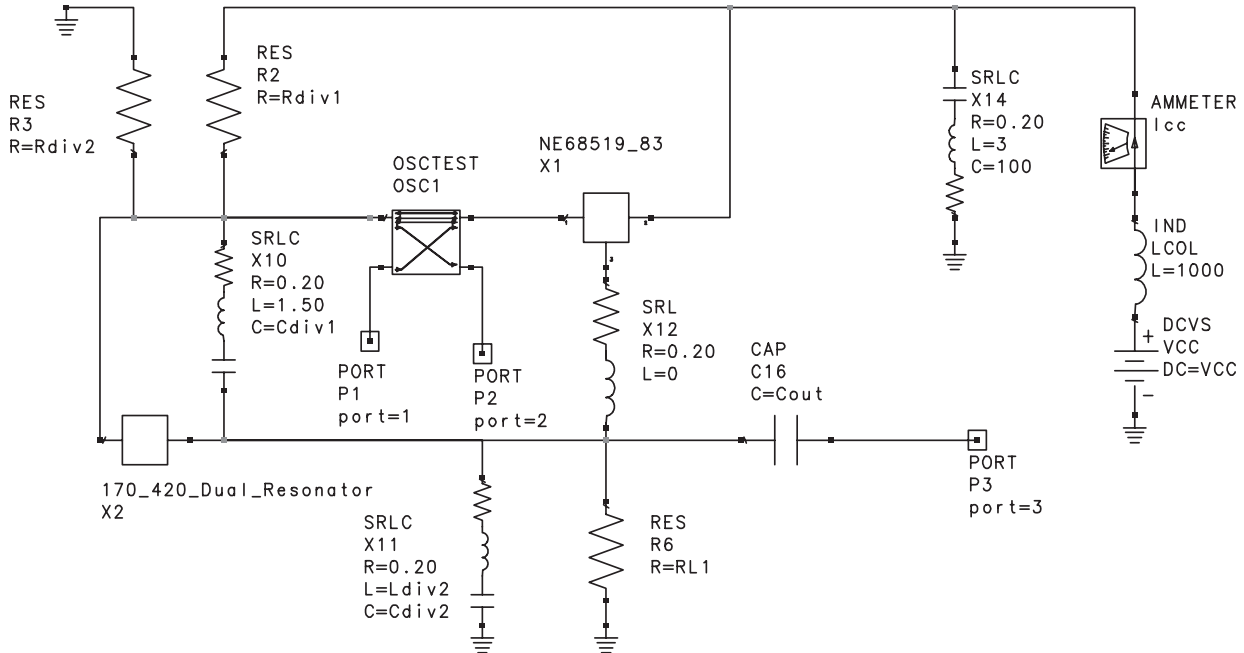
Introduction

Modern multiband cellular handsets use multiple voltage control oscillator (VCO) functions to accommodate the many down/up conversions in the intermediate frequency (IF) portion. Using separate VCOs would cause a substantial increase in cost and size of the radio frequency (RF) section. This component overload can be resolved by implementing band switchable VCOs.

In many commercial switchable VCOs, the reactive elements in the tank circuit are switched. This function is usually performed with PIN diodes. The disadvantage of this solution is that in the closed circuit state (“diode on” state) there is V_{CC} current flowing through the diode. This V_{CC} current carries electrical noise which

directly modulates the VCO frequency. Therefore, the noise spectrum may grow significantly beyond the PLL filter range. This type of switching also limits the switching to within 10–15 percent of the center frequency, due to the strong effect of PIN diode series resistance on the tank circuit losses, increasing phase noise.

In this paper, we describe a new switchable VCO solution which employs switching between separate tank circuits. This reduces the effect of PIN diode series resistance on VCO noise performance and results in virtually unlimited switching range and individual optimization of each tank circuit.



170_420_Dual_Resonator

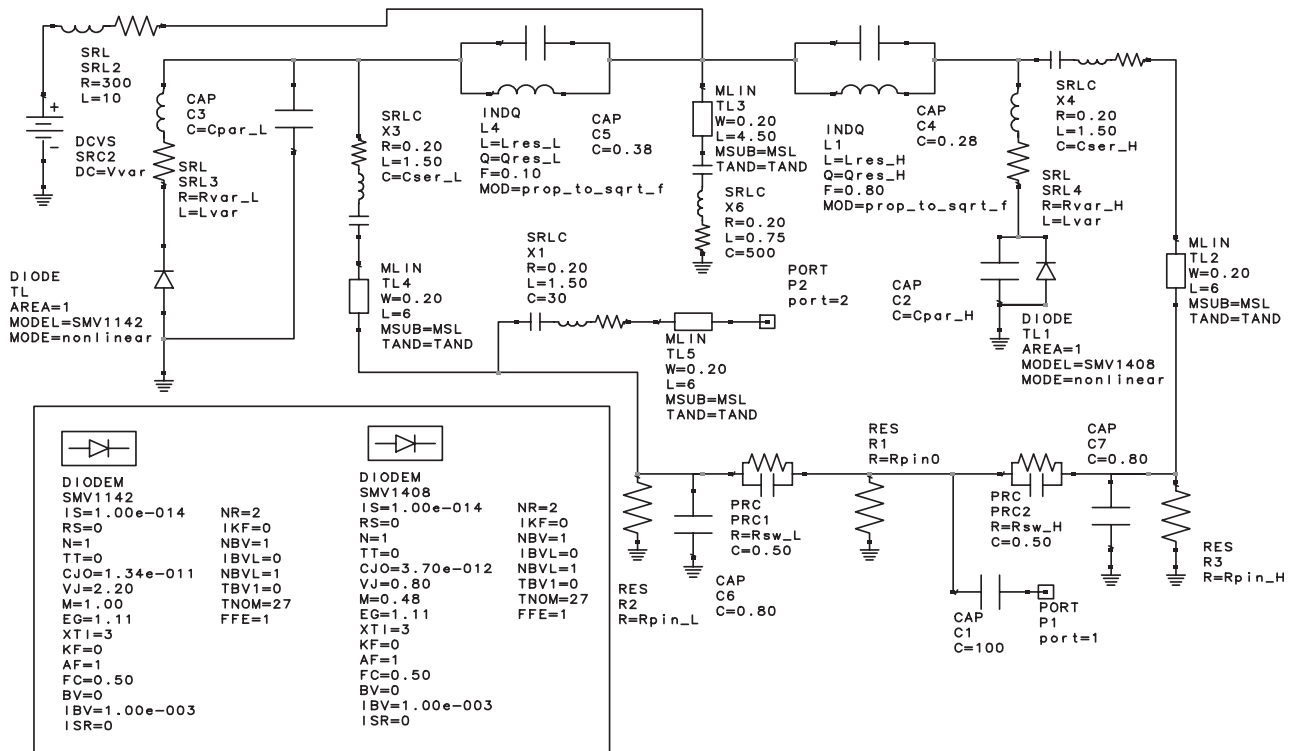


Figure 1. VCO Model

The VCO Model

In the circuit schematic in Figure 1, a traditional Colpitts structure, the varactors are connected as shunt capacitors. Tank inductors, L_4 and L_1 , providing DC bias to the varactors, are shunted to ground at the common point with capacitor X_6 . Inductors L_4 and L_1 are modeled as lossy elements (with $Q = 25$ at 100 MHz) in parallel with capacitors C_5 and C_4 of 0.38 and 0.28 pF respectively. This is typical for multilayer inductors of style 0603 (60 x 30 mil footprint), (TOKO Coils and Filters catalogue). The tank inductor values of 12 and 56 nH were optimized to fit the desired 170 MHz and 420 MHz frequency bands. Capacitor X_6 is modeled as a series RLC network with the length of transmission line TL_3 , appropriated to its position on the layout (see later). Series capacitive reactances, X_3 and X_4 are also modeled as lossy series RLC networks, with their appropriate layout-specific transmission lines, TL_4 and TL_2 . Shunt capacitors, C_7 and C_6 , are due to the effects of multiple components pads. The DC bias resistance, SRL_2 , was chosen relatively small, 300 Ω , to avoid significant thermal noise generation.

The PIN diodes were modeled as parallel RC networks, PRC_1 and PRC_2 , with switching resistances R_{SW_L} and R_{SW_H} , in the low band and high band branches respectively. The appropriate biasing resistors are shown as shunt elements to ground, R_2 , R_1 and R_3 , respectively. The truth table showing the values of R_{SW_L} and R_{SW_H} for the appropriate low/high switching is shown in Table 1.

R_{SW_L}	R_{SW_H}	State
3 Ω	3000 Ω	Low band
3000 Ω	3 Ω	High band

Table 1. Truth Table

Capacitor X_1 improves the low band matching of the tank circuit, insignificantly affecting high band performances of the tank, because of the high resistance of PRC_1 in the low band state. This component may be removed for narrower frequency switching.

The Colpitts feedback capacitances, $C_{DIV1} = 20$ pF and $C_{DIV2} = 15$ pF, were optimized to provide a reasonable power response over the switching range.

The NEC NE68519 transistor was selected for its high gain and low noise performance. The output is supplied from the emitter load resistance, RL_1 through the 20 pF coupling capacitor, modeled as a series SLC_1 component.

Figure 2 shows the Libra Test Bench. In the test bench we define an open loop gain, $K_u = V_{out}/V_{in}$, as a ratio of voltage phasors at input and output ports of an OSCTEST component. Defining the oscillation point balances the input (loop) power to provide zero gain for a zero loop phase shift. Once the oscillation point is defined, the frequency and output power may be measured. We don't recommend using the OSCTEST2 component for the closed loop analysis, since it may not always converge and does not allow clear insight into the understanding of VCO behavior. This is considered an advantage of modeling over a purely experimental study.

Figure 3 shows the Default Bench. The variables used for more convenient tuning during performance analysis and optimization are listed in a "variables and equations" component.

SMV1142-011 and SMV1408-011 SPICE Models

SPICE models for the SMV1142-011 and SMV1408-011 varactor diodes defined for the Libra IV environment, are shown in Figure 4 and Figure 5 with a description of the parameters employed.

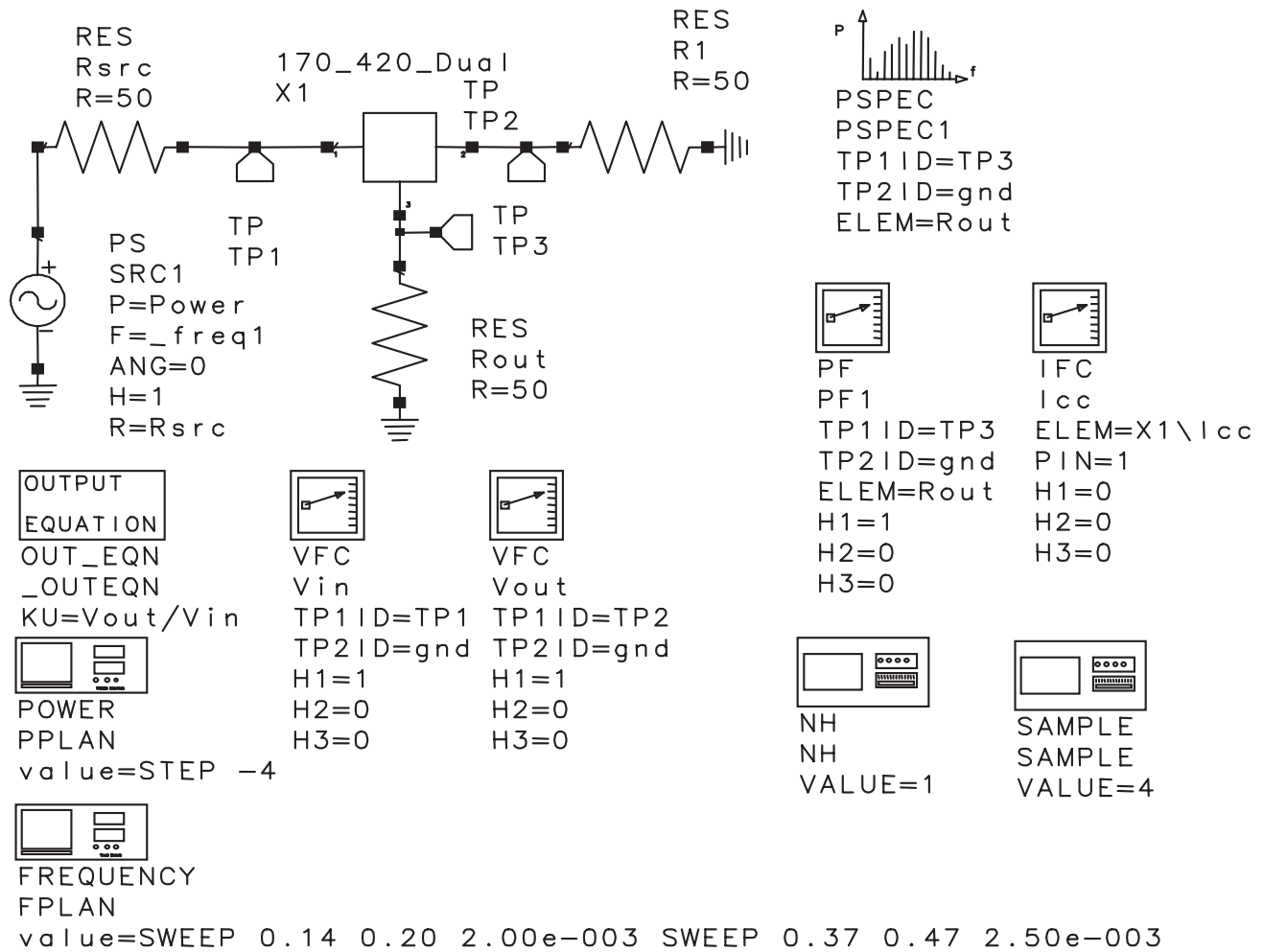


Figure 2. Libra Test Bench

<p>DATA</p> <p>TEMP Tamb TEMP=25</p>	<p>DATA</p> <p>SIGMA SIGMA_DEFAULT SIGM=0</p>	<p>UNITS</p> <p>UNITS_DEFAULT FREQ=GHz RES=Ohm COND=S IND=nH CAP=pF LNG=mm TIME=psec ANG=deg POWER=dBm VOLT=V CUR=mA DIST=mi</p>	<p>DATA</p> <p>MSUB MSL1 ER=4.20 H=0.30 T=0.08 RHO=0.75 RGH=1 COND1=cond COND2=cond2 DIEL1=die1</p>	<p>DIEL2=die12 HOLE=hole RES=resi</p>
<p>DATA</p> <p>PERM PERM_DEFAULT MUR=1 TANM=0</p>	<p>DATA</p> <p>RREF RREF_DEFAULT R=50</p>	<p>DATA</p> <p>TAND TAND TAND=0.10</p>	<p>DATA</p> <p>MSUB MSL ER=4.20 H=0.80 T=0.05 RHO=0.75 RGH=1 COND1=cond COND2=cond2 DIEL1=die1</p>	<p>DIEL2=die12 HOLE=hole RES=resi</p>
<p>Var Eqn</p> <p>VAR _VAR Cout=20 Lpull=300 Ccol_gnd=100 Lres_L=56 Lres_H=12 Qres_L=20 Qres_H=30 Vvar=0.20 VCC=3 Power=-20 Ldiv2=2.50 Rvar_L=0.70 Rvar_H=0.60 Lvar=3 Cser_L=10</p>	<p>Cser_H=8 Cpar_L=0.50 Cpar_H=0 Lpar=0.50 Cdiv2=15 Cdiv1=20 RL1=100 Rdiv1=3000 Rdiv2=6800 Rpin0=1500 Rpin_L=1500 Rpin_H=1500 Rsw_L=2 Rsw_H=3000</p>	<p>DATA</p> <p>MSUB MSL ER=4.20 H=0.80 T=0.05 RHO=0.75 RGH=1 COND1=cond COND2=cond2 DIEL1=die1</p>	<p>DIEL2=die12 HOLE=hole RES=resi</p>	

Figure 3. Default Bench

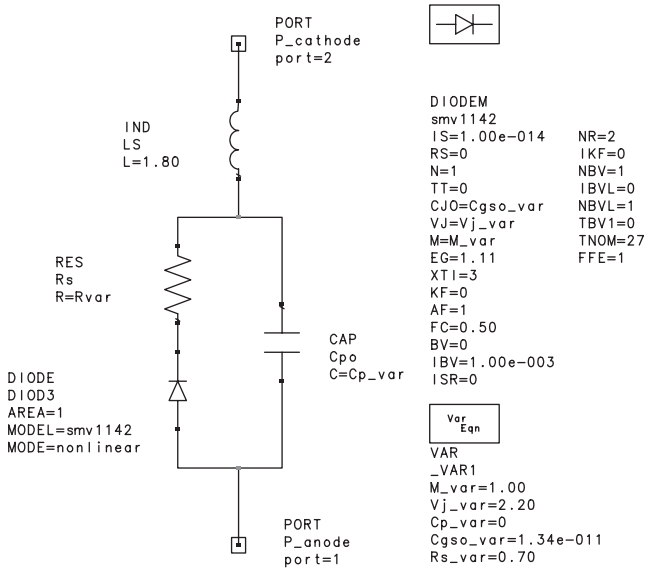


Figure 4. Spice Model for SMV1142-011

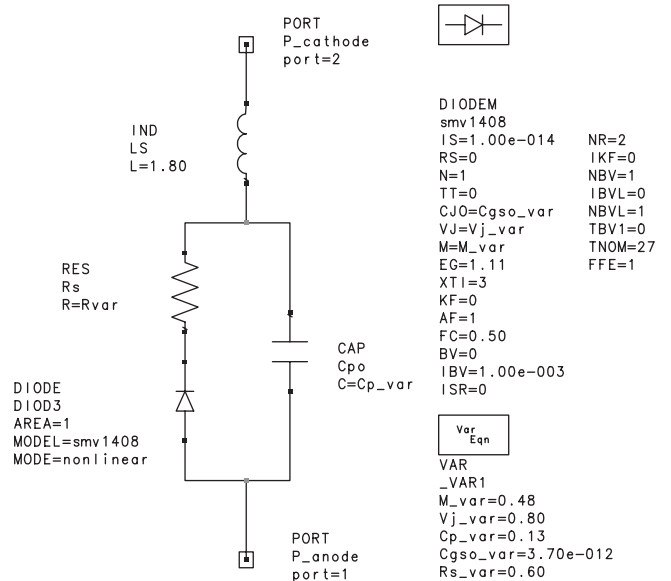


Figure 5. Spice Model for SMV1408-011

Parameter	Description	Unit	Default
I _S	Saturation current (with N, determine the DC characteristics of the diode)	A	1e-14
R _S	Series resistance	Ω	0
N	Emission coefficient (with I _S , determines the DC characteristics of the diode)	-	1
TT	Transit time	S	0
C _{J0}	Zero-bias junction capacitance (with V _J and M, defines nonlinear junction capacitance of the diode)	F	0
V _J	Junction potential (with V _J and M, (C _{J0} and M) defines nonlinear junction capacitance of the diode)	V	1
M	Grading coefficient (with V _J and M, (C _{J0} and V _J) defines nonlinear junction capacitance of the diode)	-	0.5
E _G	Energy gap (with XTI, helps define the dependence of I _S on temperature)	EV	1.11
XTI	Saturation current temperature exponent (with E _G , helps define the dependence of I _S on temperature)	-	3
KF	Flicker noise coefficient	-	0
AF	Flicker noise exponent	-	1
FC	Forward-bias depletion capacitance coefficient	-	0.5
B _V	Reverse breakdown voltage	V	Infinity
I _{BV}	Current at reverse breakdown voltage	A	1e-3
ISR	Recombination current parameter	A	0
NR	Emission coefficient for ISR	-	2
IKF	High injection knee current	A	Infinity
NBV	Reverse breakdown ideality factor	-	1
IBVL	Low-level reverse breakdown knee current	A	0
NBVL	Low-level reverse breakdown ideality factor	-	1
T _{NOM}	Nominal ambient temperature at which these model parameters were derived	°C	27
FFE	Flicker noise frequency exponent		1

Table 2. Model Parameters

Table 2 describes the model parameters. It shows default values appropriate for silicon varactor diodes that may be used by the Libra IV simulator.

According to the SPICE model in Figures 4 and 5, the varactor capacitance, C_V, is a function of the applied reverse DC voltage, V_R, and may be expressed as follows:

$$C_V = \frac{C_{J0}}{\left(1 + \frac{V_{VAR}}{V_J}\right)^M} + C_P$$

This equation is a mathematical expression of the capacitance characteristic. The model is accurate for abrupt junction varactors (like the SMV1408). The model is less accurate for hyperabrupt junction varactors because the coefficients are dependent on applied voltage. To make the above equation work better for the hyperabrupt varactors, the coefficients were optimized for the best capacitance vs. voltage fit as shown in Table 3 and Figure 6.

Note: In the Libra model shown in Figure 6, C_P is given in picofarads, while C_{G0} is given in farads to comply with the default unit system used in Libra.

Part Number	C _{J0} (pF)	M	V _J (V)	C _P (pF)	R _S (Ω)	L _S (nH)
SMV1142-011	13.4	1	2.2	0	0.7	1.8
SMV1408-011	21	25	68	0.13	0.6	1.8

Table 3. Optimized Coefficients for Capacitance vs. Voltage

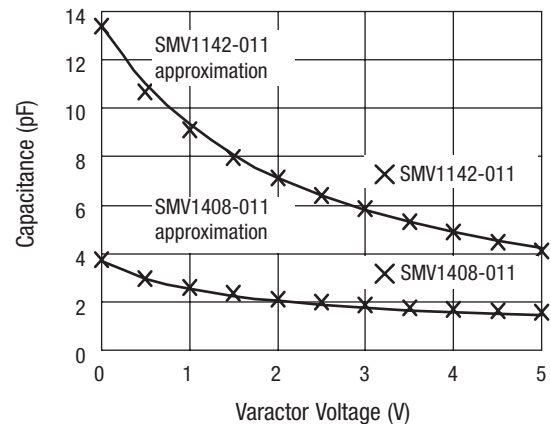


Figure 6. Capacitance vs. Voltage

VCO Design, Materials, Layout and Performance

Figure 7 shows the VCO circuit diagram.

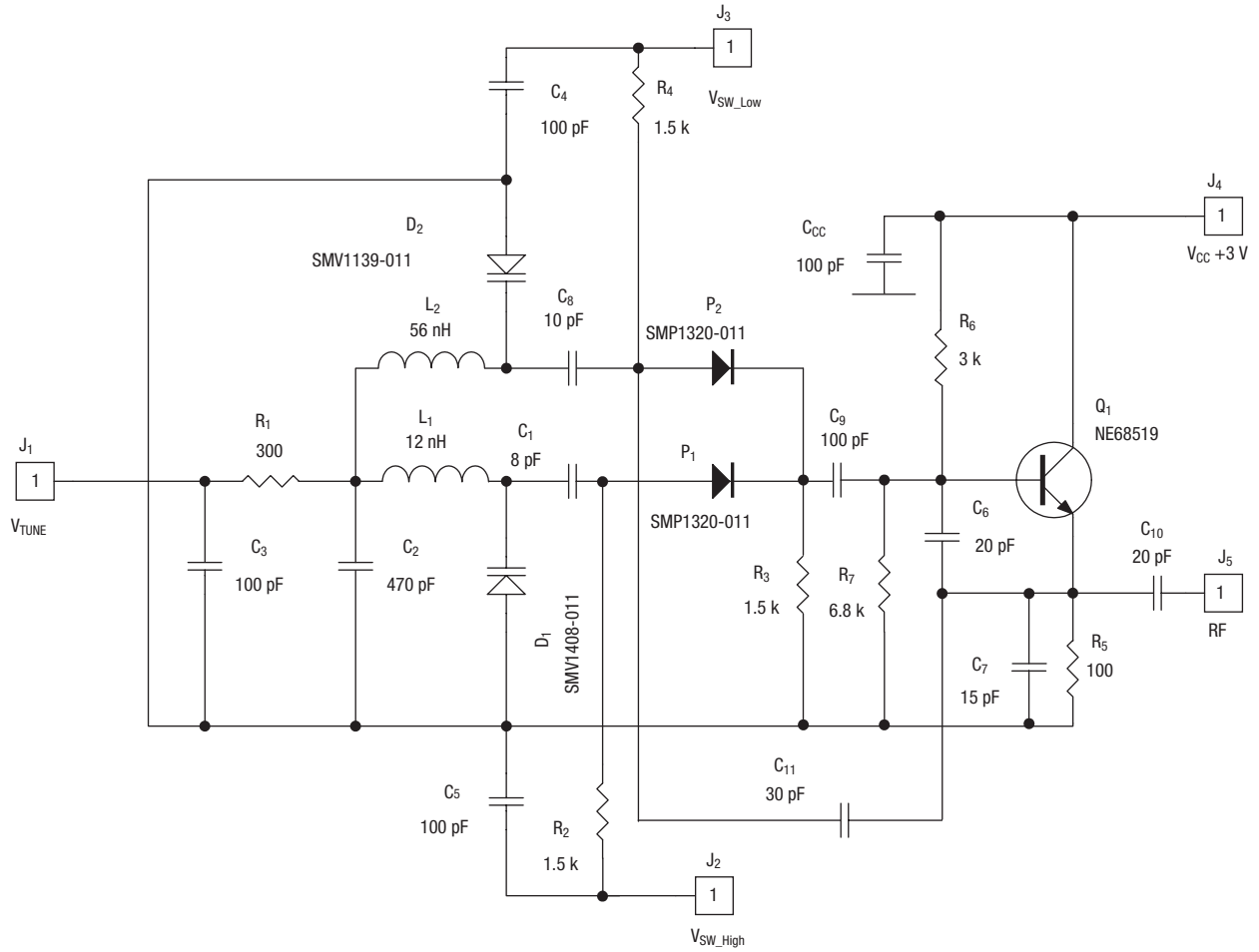


Figure 7. VCO Circuit Diagram

Table 4 shows the bill of materials used.

Designator	Value	Part Number	Footprint	Manufacturer
C ₁	8 pF	0603AU8R0JAT9	0603	AVX
C ₂	470 pF	0603AU471JAT9	0603	AVX
C ₃	100 pF	0603AU101JAT9	0603	AVX
C ₄	100 pF	0603AU101JAT9	0603	AVX
C ₅	100 pF	0603AU101JAT9	0603	AVX
C ₆	20 pF	0603AU200JAT9	0603	AVX
C ₇	15 pF	0603AU150JAT9	0603	AVX
C ₈	10 pF	0603AU100JAT9	0603	AVX
C ₉	100 pF	0603AU101JAT9	0603	AVX
C ₁₀	20 pF	0603AU200JAT9	0603	AVX
C ₁₁	30 pF	0603AU300JAT9	0603	AVX
C _{CC}	100 pF	0603AU101JAT9	0603	AVX
L ₁	12 nH	LL1608-F12NS	0603	TOKO
L ₂	56 nH	LL1608-F56NS	0603	TOKO
R ₁	300	CR10-301J-T	0603	AVX
R ₂	1.5 k	CR10-152J-T	0603	AVX
R ₃	1.5 k	CR10-152J-T	0603	AVX
R ₄	1.5 k	CR10-152J-T	0603	AVX
R ₅	100	CR10-101J-T	0603	AVX
R ₆	3 k	CR10-302J-T	0603	AVX
R ₇	6.8 k	CR10-682J-T	0603	AVX
D ₁	SMV1408-011	SMV1408-011	SOD-323	Skyworks
D ₂	SMV1142-011	SMV1142-011	SOD-323	Skyworks
P ₁	SMP1320-011	SMP1320-011	SOD-323	Skyworks
P ₂	SMP1320-011	SMP1320-011	SOD-323	Skyworks
Q ₁	NE68519	NE68519	SOT-419	NEC

Table 4. Bill of Materials

The PCB layout is shown in Figure 8. The board was made of standard, 30 mil thick, FR4 material.

Figures 9 and 10 show the measured performance of this circuit and the simulated results obtained with the model in Figure 8.

In both low and high band states there is good compliance for frequency response. Some of the difference of the measured data and the simulation is probably due to the 5–10 percent variation of circuit capacitances and inductances from their nominal values.

The low band simulated power response was in fair agreement with measured performance, assuming measurement uncertainty of ± 1 dB. The power response difference of up to 4 dB for the high band may be due to underestimated circuit component losses. For example, a decrease of inductor Q-quality from 30 to 20 decreases output power about 1 dB; an increase in resistance of switching diode from 3 to 6 Ω will decrease power to about 2 dB.

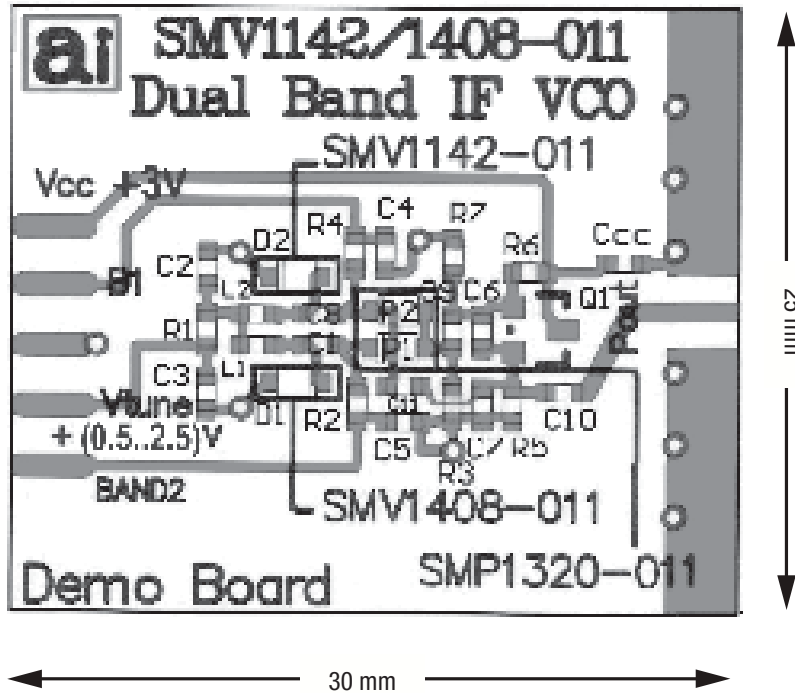


Figure 8. PCB Layout

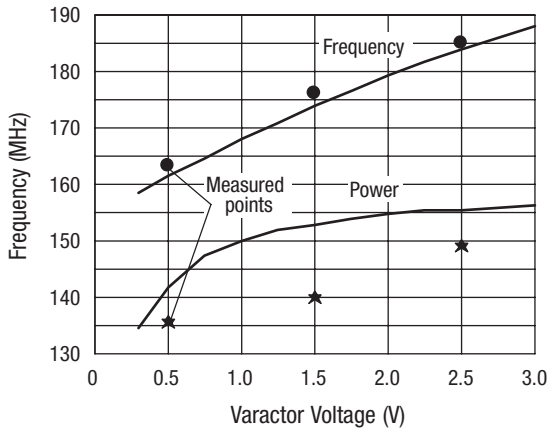


Figure 9. Low Band Measured and Simulated Results

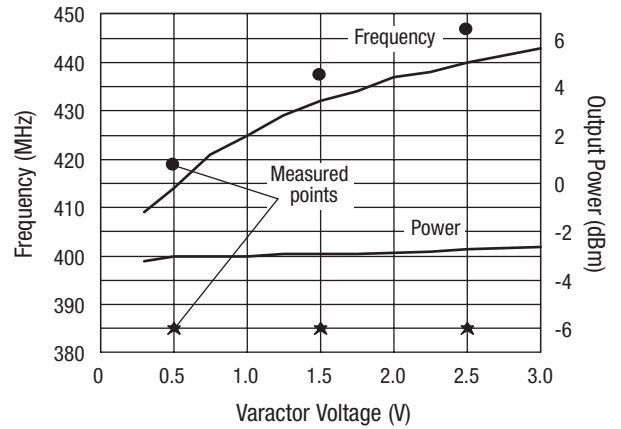


Figure 10. High Band Measured and Simulated Results

Figure 11 shows the phase noise in the high band state vs. frequency offset. It shows better than -95 dBc/Hz at 10 kHz offset. This measurement was made using the PN9000 Phase Noise Test Set, courtesy of Aeroflex Comstron, Plainview, NY (www.aeroflex.com).

Phase noise for both bands was measured with the HP8564E spectrum analyzer. At 10 kHz offset the noise was -95 dBc/Hz for both bands. It was expected that phase noise would be better at the low band. The poorer measured phase noise at the low band

may be attributed to the spectrum analyzer method where the internal noise may be too close to the measurement level. It may also be related to the wide-bandwidth matching requirement of the VCO feedback circuit, which makes it difficult to satisfy noise optimums in both bands simultaneously. Our design compromise was to balance noise performance between both bands. In any event, -95 dBc/Hz is considered acceptable for digital cellular applications.

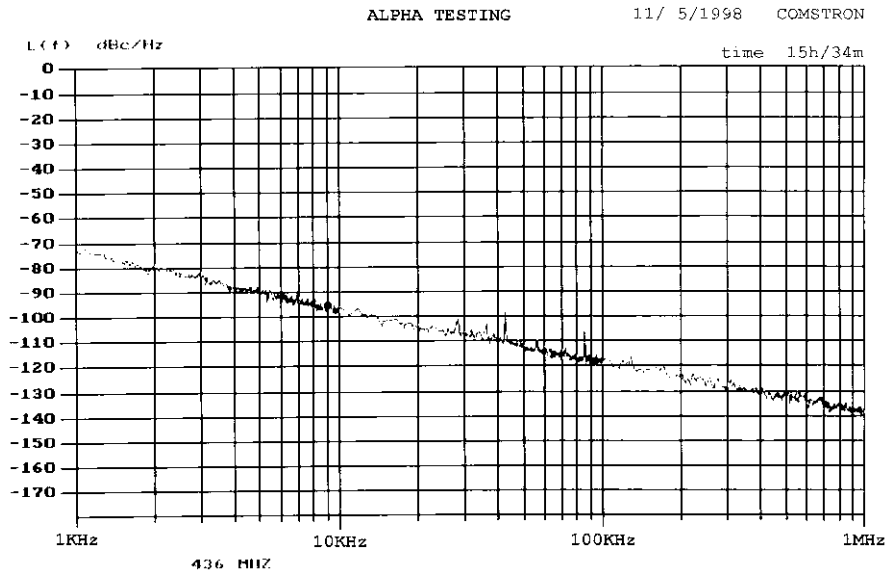


Figure 11. High Band Phase Noise vs. Frequency

List of Available Documents

1. HF Switchable VCO Simulation Project Files for Libra IV
 2. HF Switchable VCO Circuit Schematic and PCB Layout for Protel EDA Client, 1998 Version
 3. HF Switchable VCO PCB Gerber Photo-plot Files
- (For the availability of the listed materials, please call our applications engineering staff.)

VCO Related Application Notes

1. Varactor SPICE Models for RF VCO Applications
 2. A Colpitts VCO for Wideband (0.95–2.15 GHz) Set-Top TV Tuner Applications
 3. A Balanced Wideband VCO for Set-top TV Tuner Applications
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